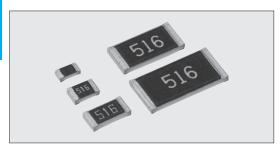


# **HV73-RT** Flat Chip Resistors For High Voltage (Anti Sulfuration)



Coating color: Black

### Features

- Excellent anti-sulfuration characteristic due to using high sulfuration-proof inner top electrode material.
- Superior to RK73 series in maximum working voltage.
- Suitable for flow and reflow solderings.
- Products meet EU-RoHS requirements. EU-RoHS regulation is not intended for Pb-glass contained in electrode, resistor element and glass.

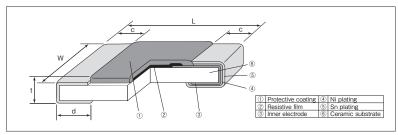
#### Applications

• Car electronics, power supply, industrial robot.

## ■Reference Standards

IEC 60115-8 JIS C 5201-8 EIAJ RC-2134C

#### Construction



#### Dimensions

Туре		Weight (g)				
(Inch Size Code)	L±0.2	W	С	d	t±0.1	(1000pcs)
1J (0603)	1.6	0.8±0.1	0.3±0.1	0.3±0.1	0.45	2.14
2A (0805)	2.0	1.25±0.1	0.4±0.2	$0.3^{+0.2}_{-0.1}$	0.5	4.54
2B (1206)	3.2	1.6±0.2				9.14
2H (2010)	5.0	2.5±0.2	0.5±0.3	$0.4^{+0.2}_{-0.1}$	0.6	24.3
3A (2512)	6.3	3.1 ±0.2				37.1

#### ■ Type Designation

#### Evample

Example						
HV73	2B	R	T	TD	1004	F
Product	Power	Characteristic	Terminal	Taping	Nominal	Resistance
Code	Rating		Surface Material		Resistance	Tolerance
	1J: 0.1W	R:Anti	T:Sn	TD:4mm pitch	D,F:4digits	D:±0.5%
	2A: 0.25W	sulfuration		punch paper	G,J:3digits	F:±1%
	2B: 0.25W			TE:4mm pitch		G:±2%
	2H: 0.5W			plastic		J:±5%
	3A:1W			embossed		
				BK : Bulk		

 $Contact \ us \ when \ you \ have \ control \ request \ for \ environmental \ hazardous \ material \ other \ than \ the \ substance \ specified \ by \ EU-RoHS.$ 

For further information on taping, please refer to APPENDIX C on the back pages

# Ratings

	Dower	Datad Ambiant	Rated Terminal	T.C.R.		Resistance F	Range (Ω)		Max. Working	Max. Overload	Taping & Q'ty/Reel		
Type	Power Rating	Temp.	Part Temp.		1.C.n. (×10 <sup>-6</sup> /K)	D:±0.5%	F:±1%	G:±2%	J:±5%		Voltage (D.C.)*1	(pcs)	
	Hatting	remp.	r art remp.	lellip. (X 10 /K)	E24 · E96	E24 · E96	E24	E24	voitage	VUITAGE (D.C.)	TD	TE	
1 J	0.1W	70℃	80℃	±100*2	_	10k~10M	10k~10M	10k~10M	350V	500V	5,000	-	
2A	0.25W	70°C	100℃	±100	100k~1M	100k~10M	100k~10M	100k~10M	400V	400\/	800V	5,000	_
ZA	0.25	700	100 C	±200	_	_	_	11M~51M		8007	5,000	_	
2B	0.05W	70°C	100℃	±100	100k~1M	100k~10M	100k~10M	100k~10M	0001/	1000V	F 000		
26	0.25W	700	1000	±200	_	_	_	11M~51M	800V	10007	5,000	_	
2H	0.5W	70°C	90°C	±100	100k~1M	100k~10M	100k~10M	100k~10M	2000V (D.C.)	3000V	_	4,000	
ΔΠ	0.500	700	900	±200	_	_	_	11M~51M	2000V (D.C.)	30000	_	4,000	
24	1W	70°0	105°C	±100	43k~1M	43k~10M	43k~10M	43k~10M	3000V (D.C.)	4000V		4.000	
3A	IVV	70°C	105℃	±200	_	10.2M~20M	11M~20M	11M~51M	3000V (D.C.)		-	4,000	

Operating Temperature Range :  $-55^{\circ}\text{C} \sim +155^{\circ}\text{C}$ 

Rated voltage= $\sqrt{\text{Power Rating} \times \text{Resistance value}}$  or Max. working voltage, whichever is lower.

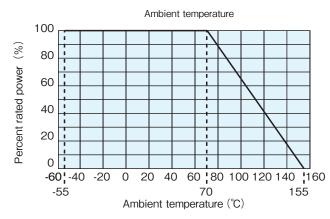
\*1 Max. overload voltage is specified by D.C. voltage.

\*2 Cold T.C.R. (-55°C $\sim+25$ °C) of 1.02MΩ  $\sim$ 10MΩ is  $\pm200\times10^{-6}$ /K.

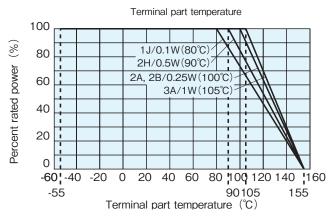
If any questions arise whether to use the "Rated Ambient Temperature" or the "Rated Terminal Part Temperature" in your usage conditions, please give priority to the "Rated Terminal Part Temperature". For more details, please refer to "Introduction of the derating curves based on the terminal part temperature" on the beginning of our catalog.



#### Derating Curve



For resistors operated at an ambient temperature of 70°C or higher, the power shall be derated in accordance with the above derating curve.



When the terminal part temperature of the resistor exceeds the rated terminal part temperature shown above, the power shall be derated according to the derating curve. \*\*Please refer to "Introduction of the derating curves based on the terminal part temperature" on the beginning of our catalog before use.

#### Performance

Toot Itomo	Performance Requirements	ΔR± (%+0.1Ω)	Took Makhada		
Test Items	Limit	Typical	Test Methods		
Resistance	Within specified tolerance	_	25°C		
T.C.R.	Within specified T.C.R.	_	+25°C/−55°C and +25°C/+125°C		
Overload (Short time)	2	0.5	Rated voltage (D.C.) ×2.5 for 5s		
Resistance to soldering heat	1	0.5	260°C±5°C, 10s±1s		
Rapid change of temperature	0.5: $(10k\Omega \le R \le 10M\Omega)$ 1: $(11M\Omega \le R \le 51M\Omega)$	0.3: $(10k\Omega \le R \le 10M\Omega)$ 0.5: $(11M\Omega \le R \le 51M\Omega)$	-55°C (30min.) /+125°C (30min.) 100 cycles		
Moisture resistance	2	0.75	40°C±2°C, 90%~95%RH, 1000h 1.5h ON/0.5h OFF cycle		
Endurance at 70°C or rated terminal part temperature	2	0.75	70°C±2°C or rated terminal part temperature ±2°C 1000h 1.5h 0N/0.5h 0FF cycle		
High temperature exposure	2	0.3	+155°C, 1000h		
Sulfuration test	5	0.2	Soaked in industrial oil with sulfur substance 3.5% contained 105°C ±3°C 500h		

Please refer to conventional products for characteristic data such as temperature rise.

#### ■Precautions for Use

- Max. overload voltage is specified by D.C. voltage. When using in A.C. voltage, the peek value of A.C. voltage shall not exceed the Maximum overload voltage.
- The substrate of chip resistors is alumina. Cracks may occur at the connection of solder (solder fillet portion) due to the difference of the coefficient of thermal expansion from a mounting board when heat stress like heat cycle, etc. are repeatedly given to them. Care should be taken to the occurrence of the cracks when the change in ambient temperature or ON/OFF of load is repeated, especially when large types of 2H/3A which have large thermal expansion and also self heating. By general temperature cycle test using glass-epoxy (FR-4) boards under the maximum/minimum temperatures of operating temperature range, the crack does not occur easily in the types of 1J~2B, but the crack tends to occur in the types of 2H/3A. The occurrence of the crack by heat stress may be influenced by the size of a pad, solder volume, heat radiation of mounting board etc., so please pay careful attention to designing when a big change in ambient temperature and conditions for use like ON/OFF of load can be assumed.